

School of Computer Science & Engineering

**Trustworthy Systems Group** 

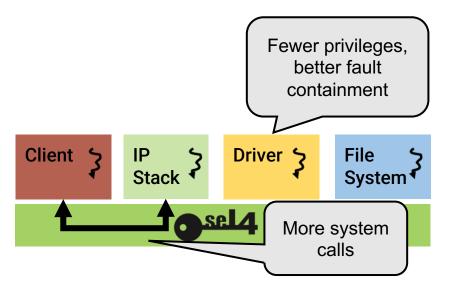
## The seL4 Device Driver Framework

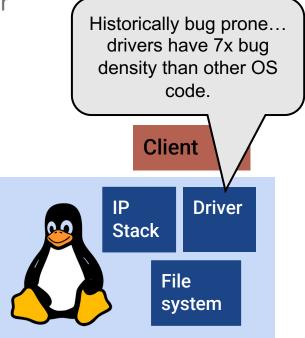
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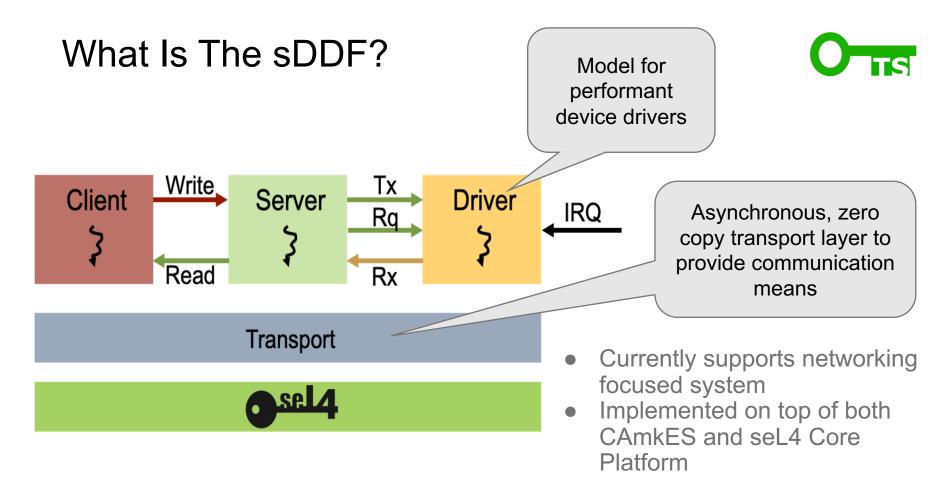
## What Is The seL4 Device Driver Framework?

Framework to provide interfaces and protocol for writing performant device drivers as seL4 user level programs.

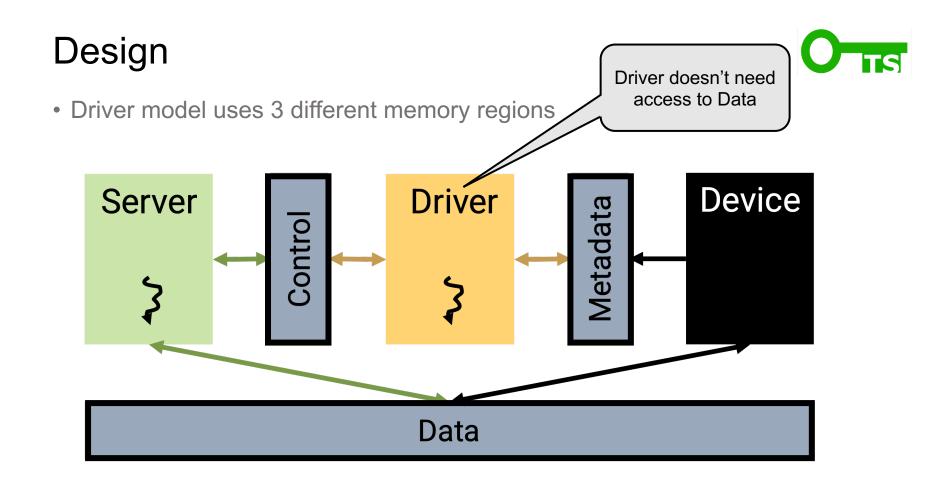










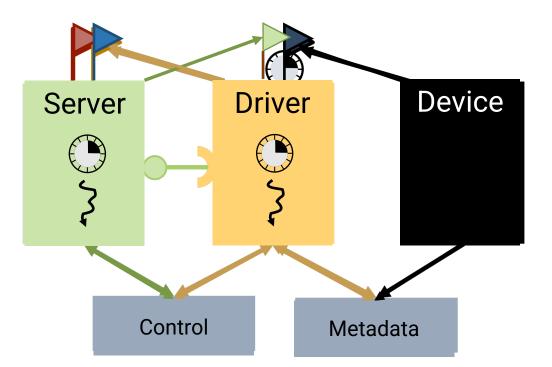




**UNSW** 

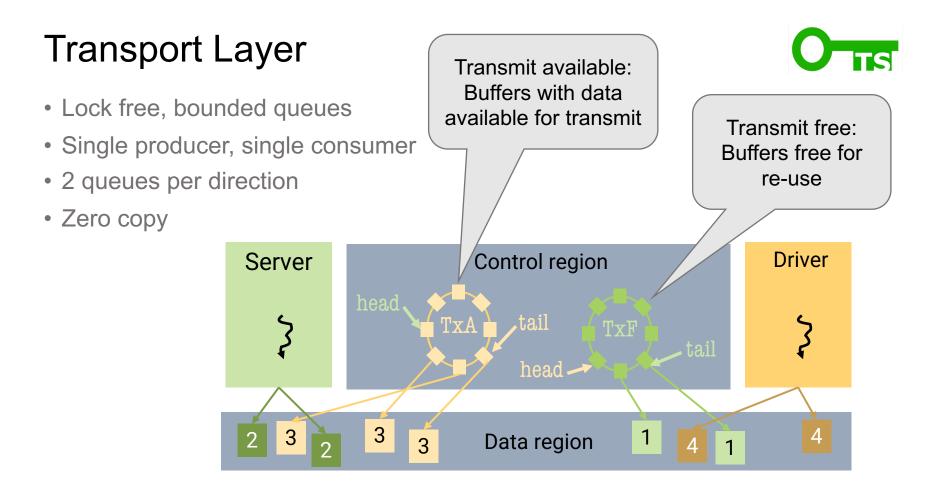
## Driver Model

- Purely reactive
- Single threaded
- Simple
- Active ... or passive?











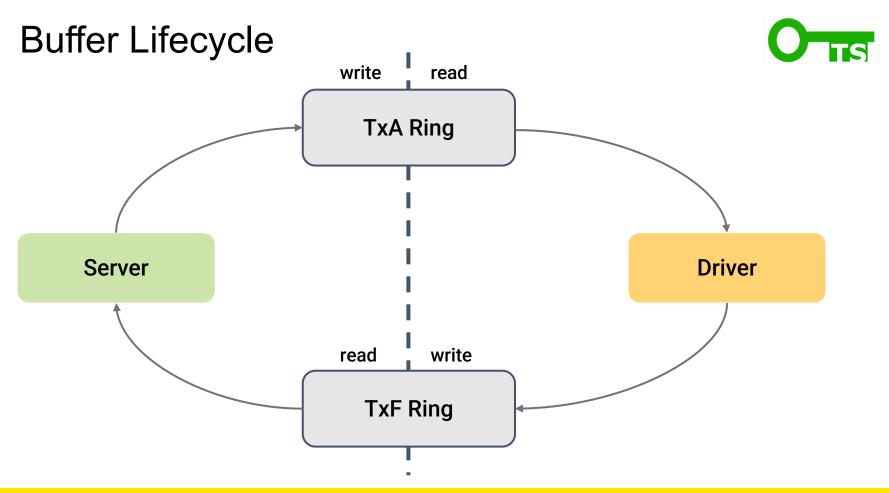
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## **Transport Layer**



```
void enqueue(struct buffer descr *buffer,
                                                          struct ring buffer *ring) {
struct buffer descr {
                                               assert(!full(ring));
                                               ring->buffer[ring->head % RING_SIZE] = *buffer;
 void *address;
 size t length;
                                               barrier();
                                               ring->head += 1;
                                            }
struct ring_buffer {
 uint32 t head;
                                            struct buffer desc* dequeue(struct ring buffer *ring) {
 uint32 t tail;
                                              assert( !empty(ring) );
                                              struct buffer descr *buf = \
 struct buffer descr buffer[RING SIZE];
                                                       ring->buffer[ring->tail % RING SIZE];
                                              barrier();
                                              ring->tail += 1;
       Barrier ensures no writes are re-
     ordered by the compiler or processor
               across this point
```



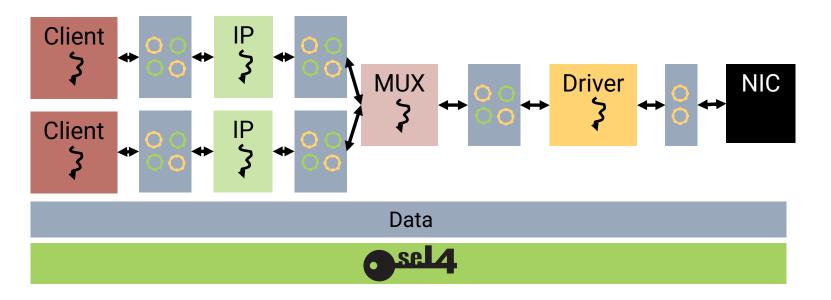




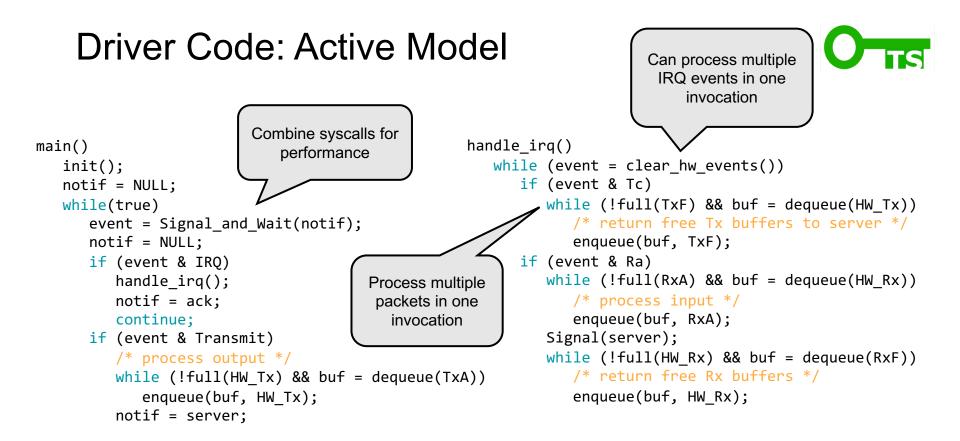
### **Transport Architecture Scales**



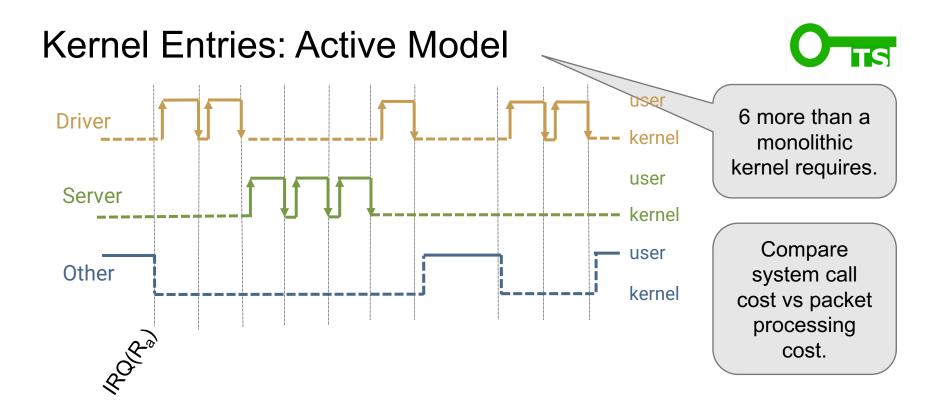
- Components can run on separate cores
- Only MUX and driver are trusted.













**UNSW** 



## **Rate Limiting**



- Driver runs at highest priority to minimise round trip times
- Regardless of active/passive model, the protocols for transmit are synchronous.
- Requires limiting CPU time by configuring budgets and periods of scheduling contexts used by higher priority components. And/Or limiting the queue size.





# Performance



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### packets

- IPbench sends UDP packets and measures
- to calculate CPU Utilisation

Built with

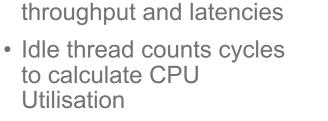
CAmkES and

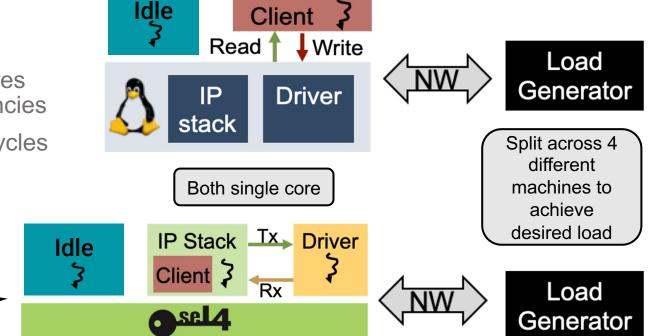
**Core Platform** 



## Set Up

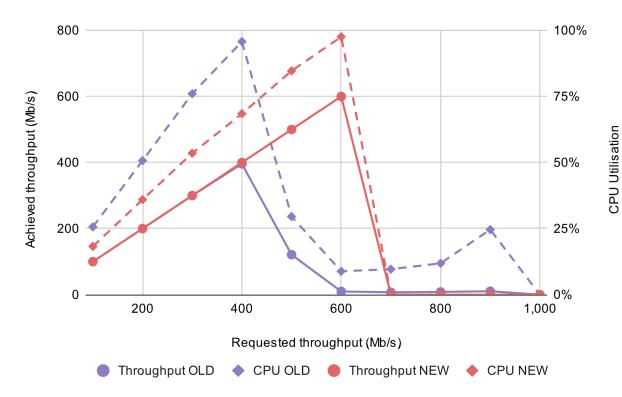
Client just echoes







#### Old Transport vs New: CAmkES Networking Performance

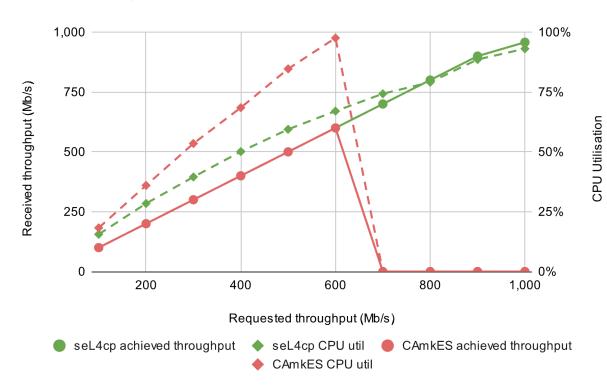




- Simpler transport
- Adjustment of priorities
- Showed 50% improvement!
- But could not combine system calls easily...



### seL4 Core Platform vs CAmkES Networking Performance



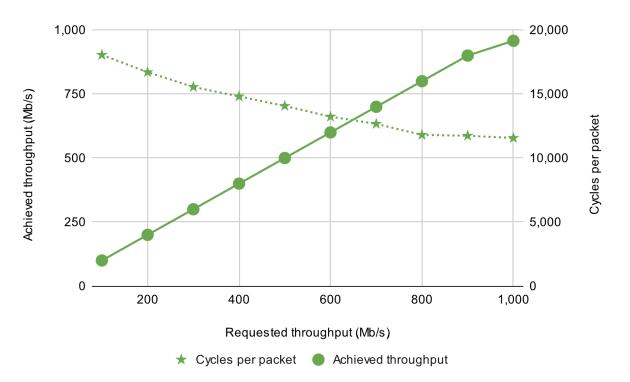


- Reduced kernel entries
- Simpler platform
- Showed 70% improvement, 150% over old CAmkES!
- Limited drivers budget to remove performance collapse.



### seL4 Packet Processing Cost

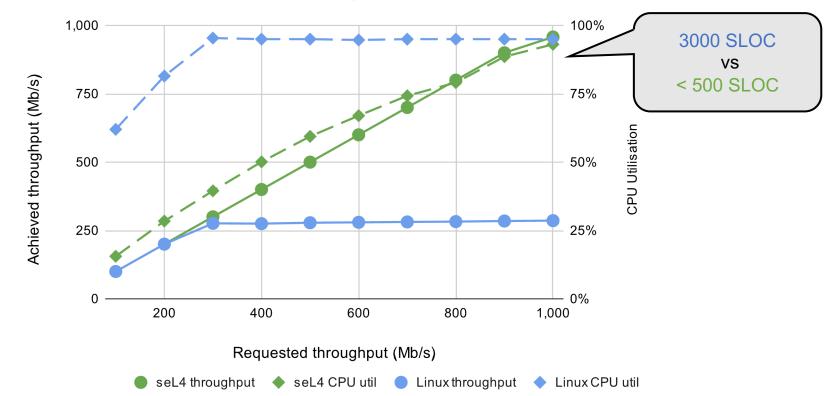






## seL4 vs Linux Networking Performance

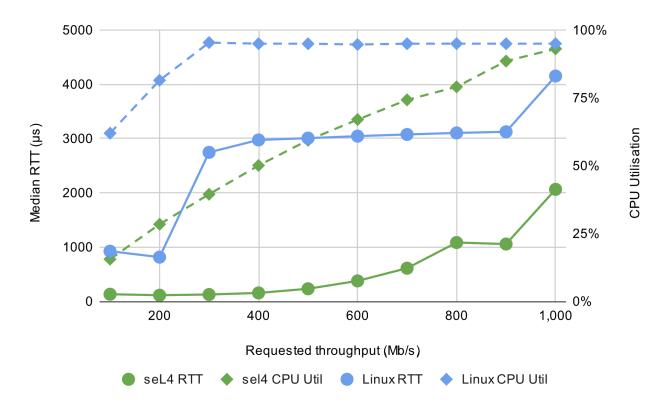






## seL4 vs Linux RTT Comparison

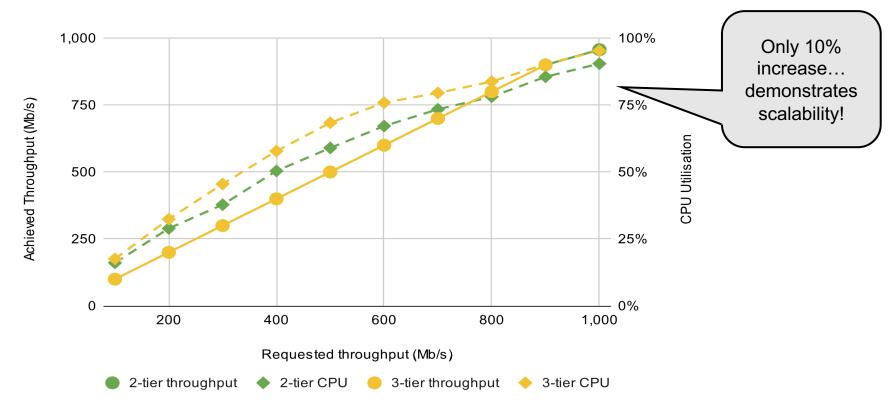






## Cost Of A Module Crossing

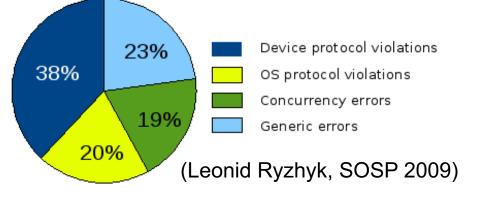






#### • Significant performance boost: 150% improvement on old model

- Smaller latencies and higher throughput achieved over Linux
- Simple works! Eliminates concurrency bugs and will help verification effort.







## Further Work



- Evaluate the passive driver model.
- Benchmark a more complex client.
- Investigate the optimal budgets and periods for different scenarios (eg. Asymmetric traffic).
- Evaluate what an optimised IP stack might look like.
- Design and build a multiplexor.
- Extend the sDDF to support other device classes.
- Evaluate multicore set up.









- Current state of the code as implemented for seL4 Core Platform can be found here: <u>https://github.com/lucypa/sDDF</u>
- RFC: <u>https://sel4.atlassian.net/browse/RFC-12</u>





## Questions?



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